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09/974814
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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

S.I.	O.I.P.E.	PATENT DATE
SCANNED <i>GP</i>	Q.A. <i>Am</i>	

APPLICATION NO. 09/974814	CONT/PRIOR F	CLASS 438	SUBCLASS 682	ART UNIT 2814	EXAMINER Walt J. Harty
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APPLICANTS
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 Fumiaki Endo
 Masahori Kojima
 Katsuhiko Uchimura

TITLE
 Semiconductor integrated circuit device and the process of the same

PTO-204
 1289

ISSUING CLASSIFICATION							
ORIGINAL			CROSS REFERENCE(S)				
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
	_____ (Date)			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		ISSUE FEE	
	_____ (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	
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